IN THE TITLE

Please amend the title as follows: A SEMICONDUCTOR INTEGRATED CIRCUIT ~~DEVICE~~HAVING OSCILLATORS OR OSCILLATION CIRCUITS CONNECTED TO A WIRING LINE AT CONNECTION POINTS WITH INTERVALS IN LENGTH THEREBETWEEN.

IN THE SPECIFICATION

Page 1, before the first line, add the paragraph:

This is a continuation application of U.S. Serial No. 09/141,343, filed August 27, 1998 (now allowed).

Page 9, first full paragraph, lines 3-18, replace the paragraph with:

Besides, although the inverters 110 ~ 11m, 120 ~ 12m, and 1n0 ~ 1nm constituting the respective ring oscillators are all shown in Fig. ~~3~~4 to be of the same type, they need not always be the same type. If they are not the same, the respective ring oscillators oscillate with substantially identical frequency, but not in the identical phase in the steady state as in the foregoing. The respective ring oscillators oscillate in synchronism while having the phases $\delta 1 \sim \delta n$. To the contrary, the phases $\delta 1 \sim \delta n$ can be changed by adjusting the sorts (load driving capabilities, etc.) of the inverters of the respective ring oscillators. The phases $\delta 1 \sim \delta n$ of the respective ring oscillators can be brought into substantially identical phase by adjusting the sorts of the inverters of the

respective ring oscillators through the utilization of such a property, even when all the distances between the respectively adjacent nodes are not set at the equal distances l.